

REMARKS

This responds to the Office Action mailed on June 10, 2005. Claims 1, 8 and 15 are amended and claims 21-23 are added; as a result, claims 1-23 are now pending in this application.

Drawing Objections and §112 Rejection of the Claims

The drawing objection and the rejection of some of the claims under 35 USC 112 (first paragraph) are related and will be addressed together. In particular, the objection and rejection related to the F and G functions.

The Office Action objected to the drawings under 37 CFR 1.83(a):

The drawings must show every feature of the invention specified in the claims. Therefore, the details of the F and G functions must be shown or the feature(s) canceled from the claim(s).

Office Action at ¶1.

Claims 4, 6-7, 12-14, 18, and 19-20 were rejected under 35 USC § 112, first paragraph, as failing to comply with the written description requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention. In particular, the Office Action indicated that such claims which contain the F and G functions

do not adequately describe what the functions are that perform the functions as claims, and therefore do not describe the claimed subject matter in a way that would enable one of ordinary skill in the art to make and or use the claimed subject matter.

Office Action at ¶4.

Applicant respectfully traverses this drawing objection and rejection of such claims under 35 USC 112 (first paragraph).

Under 37 CFR 1.83(a),

[t]he drawing in a nonprovisional application must show every feature of the invention specified in the claims. However, conventional features disclosed in the description and claims, where their detailed illustration is not essential for a proper understanding of the invention, should be illustrated in the drawing in the form of a graphical drawing symbol or a labeled representation (e.g., a labeled rectangular box). (emphasis added).

37 CFR 1.83(a)

Applicant respectfully submits that a detailed illustration of the F and G functions are not required in the drawings. The drawings in conjunction with the detailed description are sufficient to enable one of ordinary skill in the art to have a “proper understanding of the invention” and to make and or use the claimed subject matter. The F function is described as a function that “determines the value to write to flip-flop 215.” Application at ¶14. One of ordinary skill in the art would understand that this function can be any type of logic (such as hardware, software, firmware, or a combination thereof) for determining a value. For example, the F function may be a logic for writing a value based on a comparison operation, an assignment instruction for writing to register, etc.

With regard to the G function, this function “determines when to write the value from the F function to the flip-flop 215.” Application at ¶14. One of ordinary skill in the art would understand that this function can be any type of logic (such as hardware, software, firmware, or a combination thereof) for determining when to write a value. For example, the G function may be a conditional or comparison logic that writes the value when a given condition is satisfied.

Accordingly, the F and G functions are sufficiently described and illustrated to reasonably convey to one skilled in the relevant art that the inventor(s) how to make or use the claimed subject matter. Therefore, Applicant respectfully requests that the drawing objection and the rejection of claims 4, 6-7, 12-14, 18, and 19-20 under 35 USC § 112, first paragraph be withdrawn.

§102 Rejection of the Claims

Claims 1-3, 5, 8-11, and 15-17 were rejected under 35 USC § 102(b) as being anticipated by Vashi (U.S. 6,219,819). Applicant has amended claim 1. In particular, Applicant has amended

Among the differences, claims 1 and 15 (as amended) recite “creating a bypass logic in a digital circuit design that is without bypass logic, wherein creating comprises transforming a conditional state element into a logically redundant element in the digital circuit design.” With regard to claim 8, among the differences, claim 8 (as amended) recites “wherein the processor is to place a bypass logic into the circuit design that is without bypass logic through a transformation of a conditional statement into a logically redundant element within the circuit design.” Applicant respectfully submits that the Vashi reference does not disclose such a limitation. The Vashi reference relates to allowing “a design implemented in a configurable FPGA to be implemented in a non configurable (HardWire) device without problems due to changes in timing.” Vashi at column 2, lines 57-60.

Accordingly, because the cited reference does not disclose all of the claim limitations, Applicants respectfully submit that the rejection of claims 1, 8 and 15 under 35 U.S.C. §102 has been overcome. Claims 2-3, 5; claims 9-11 and claims 16-17 depend from, respectively, claims 1, 8 and 15 and distinguish the reference for at least the same reason.

New Claims

Applicant has added new claims 21-23. Applicant respectfully submits that no new matter has been added and that such claims are at least supported by the description of Figures 1A-1E.

Conclusion

Applicant respectfully submits that the claims are in condition for allowance and notification to that effect is earnestly requested. The Examiner is invited to telephone Applicant's attorney ((612) 371-2103) to facilitate prosecution of this application.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

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By his Representatives,

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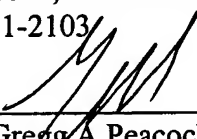
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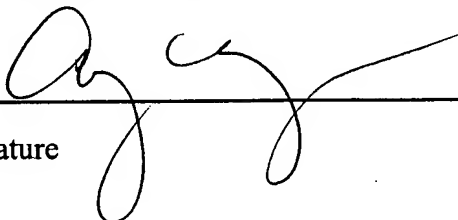
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Date 12-9-05

By 
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 9th day of December, 2005.

Amy Moriarty
Name


Signature